



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/679,041
Applicant : Robert A. Kennedy et al.
Filed : October 3, 2003
Title : Front-Accessible Communications Port for Enclosed
Electrical Equipment

TC/A.U. : Unassigned
Examiner : Unassigned

Docket No. : SPL-44 / 47181-00274USPT
Customer No. : 23569

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§1.97 and 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
37 C.F.R. 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: MS DD, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below:

12-30-03
Date

Signature

Dear Commissioner:

In compliance with the duty of disclosure under 37 C.F.R. §1.56, it is respectfully requested that this Information Disclosure Statement be entered and the reference(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

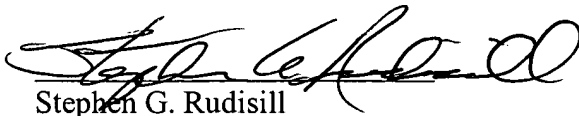
In accordance with 37 C.F.R. § 1.98(d), copies of the listed references are enclosed.

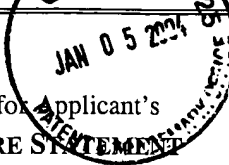
In accordance with 37 C.F.R. §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information disclosed is, or is considered to be, prior art with respect to the present application or material to patentability as defined in 37 C.F.R. §§ 1.56.

The present Information Disclosure Statement is being filed within three months of the filing date of this application and hence is believed to be timely in accordance with 37 C.F.R. § 1.97(b). Accordingly, no fees are believed to be due in connection with the filing of this Information Disclosure Statement. However, should any fees be deemed necessary (except payment of the issue fee), the Commissioner is authorized to charge any deficiency or to credit any overpayment to Jenkins & Gilchrist Account No. 10-0447/47181-00274USPT.

Respectfully submitted,

12/30/03
Date


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Form PTO-1449 (modified) List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT	Atty. Docket No.: SPL-44 / 47181-00274USPT	Serial No.: ????????
Applicant: Robert A. Kennedy et al.		Filing Date: October 3, 2003
Page 1 of 2		Group: Unassigned

U.S. Patent Documents							
Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A01						
	A02						
	A03						
	A04						
	A05						
	A06						
	A07						
	A08						
	A09						
	A10						

Foreign Patent Documents							
Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B01						
	B02						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)		
Exam. Init.	Ref. Des.	Citation
	C01	PowerLogic Circuit Monitor – Series 2000, Class 3020, Square D/Schneider Electric Product Literature, 6 pages, dated 1999.
	C02	PowerLogic Series 4000 Circuit Monitor with Transient Detection, Square D/Schneider Electric Product Literature, 2 pages, dated June, 2001.
	C03	

EXAMINER:	DATE CONSIDERED:
<small>EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.</small>	